

FORM PTO/SB/08A/B (10-01)
Substitute for PTO-1449A/B

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

(use as many sheets as necessary)

Attorney Docket Number	39521/RJP/B600
Application Number	09/819,049
Filing Date	January 20, 1998
Applicant(s)	Henry Samueli, et al.
Group Art Unit	Not Assigned
Examiner Name	Not Assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
Dr.		3,962,637	06-08-1976	Motley et al.
		4,253,184	02-24-1981	Gitlin et al.
		4,599,732	07-08-1986	LeFever
		5,386,239	01-31-1995	Wang et al.
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		5,412,352	05-02-1995	Graham
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶ (✓)

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
Dr.		SAMUELI, et al, "A VLSI Architecture for a High-Speed All-Digital Quadrature Modulator and Demodulator for Digital Radio Applications", IEEE Journal on Selected Areas in Communications, Vol. 8, No., 8, pp. 1512 - 1519, 10/1990
Dr.		WONG, et al, "A 200-MHz All-Digital QAM Modulator and Demodulator in 1.2- μ m CMOS for Digital Radio Applications", IEEE Journal of Solid State Circuits, Vol. 26, No. 12, pp. 1970 - 1980, 12/1991
Dr.		SAMUELI, et al, "VLSI Architectures for a High-Speed Tunable Digital Modulator/Demodulator/Bandpass-Filter Chip Set", 0-7803-0593-0/92 IEEE, pp. 1065 - 1068, 1992
Dr.		SAMUELI, et al, "VLSI Architectures and Circuits for High Bit-Rate Digital Modems, Adaptive Equalizers and Frequency Synthesizers", 1992 IEEE Architectures and Circuits, pp. 211 - 220, 3/1992

EXAMINER SIGNATURE		DATE CONSIDERED	5/18/05
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EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
dv.		JOSHI, et al, "A VLSI Architecture for a Single-Chip 5-MBAUD QAM Receiver", IEEE Global Telecommunications Conference (Orlando, FL), pp. 1265 -1268, 12/1992
dv.		DANESHRAD, et al, "A Carrier and Timing Recovery Technique for QAM Transmission on Digital Subscriber Loops", IEEE ICC '93 (Geneva, Switzerland), pp. 1804 -1808, 5/1993
dv.		LU, et al, "A 60-Mbaud Adaptive Transversal Equalizer in 1.0- μ m CMOS for QAM Digital Modems", Proceedings of the IEEE 1993 Custom Integrated Circuits Conference, 5/1993
dv.		LU, et al, "A 60-MBd, 480-Mb/s, 256-QAM Decision-Feedback Equalizer in 1.2- μ m CMOS", IEEE Journal of Solid-State Circuits, Vol. 28, No. 3, pp. 330 - 338, 3/1993

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